

FLASH MEMORY 450 FLASH MEMORY ARRAY REDUNDANCY CAM **INFORMATION** DEF. ADDR. TEST PINS 420 **BLOCK** <u>410</u> ROM 430 ADDRESSABLE MEMORY 440 ARRAY PLANE 210 <u>412</u> PHY. ADDR. I/Os EXTERNAL ADDRESS ARRAY PLANE 210 412

FIG. 4B

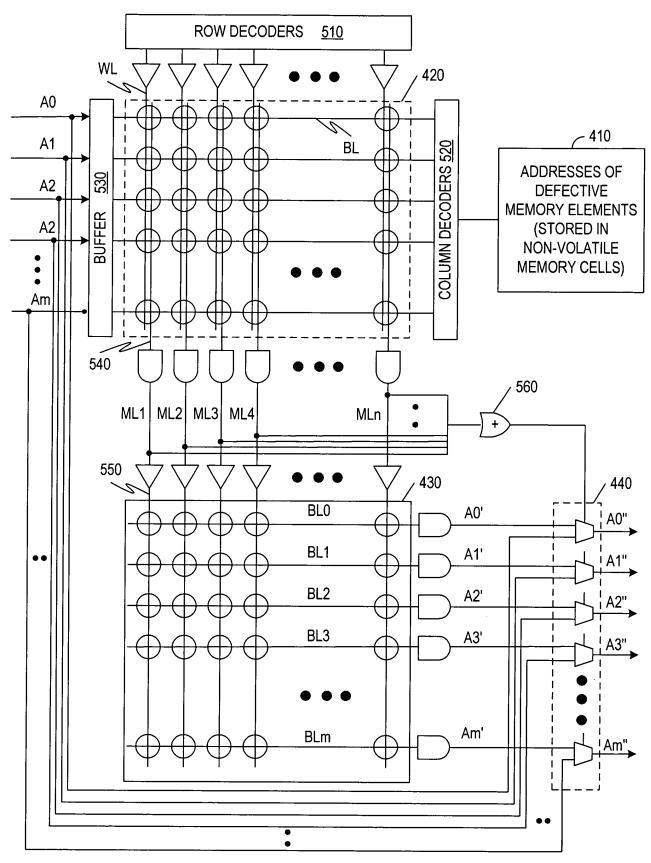


FIG. 5

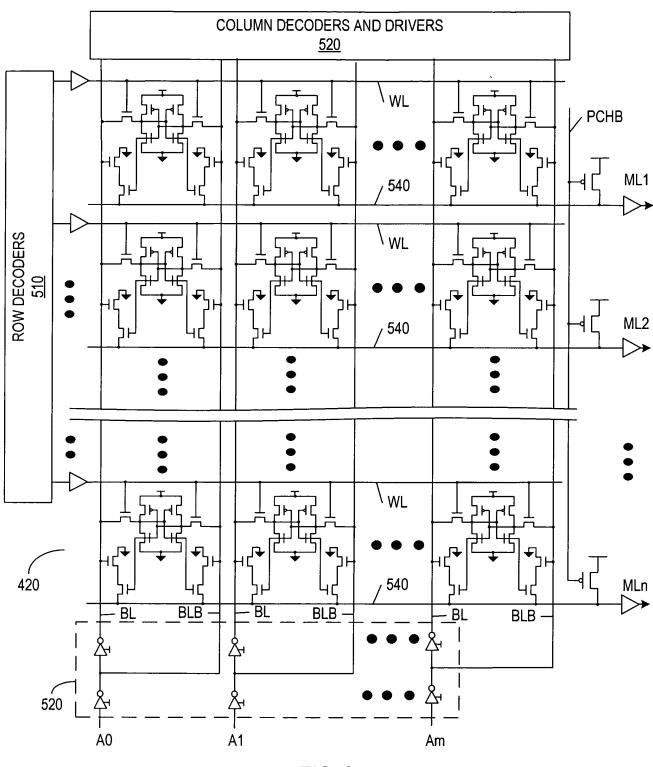


FIG. 6

